

SEFEA MEMORY KEY

MK40C Key

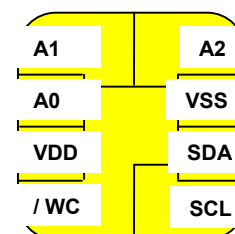
MK Series

/// E²PROM 2 Kbits M24C02 type



Real size

I²C™ protocol



MK40C Pin out

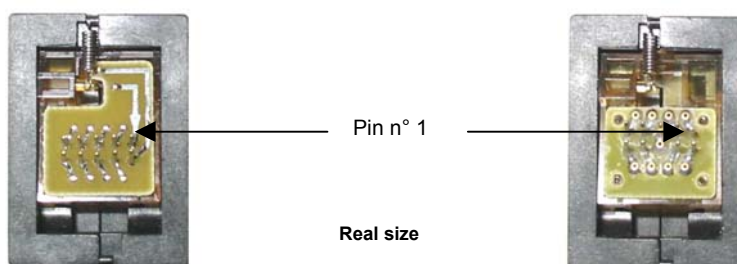
/// MAIN FEATURES

2 Kbits E²PROM I²C™-standard serial access memory (two-wire two-way Data Bus and Serial Clock for internal operations), incorporating a unique 4-bit (1010) ID code that matches the I²C™ definition, as well as a Chip Select system (3 bits with 3 inputs) enabling up to 8 memories to be connected to the same bus. (Full Data Sheet available upon request.)

/// MACRO-INSTRUCTIONS

Byte Write	: Write data into the addressed byte.
Page Write	: Allows up to 8 bytes to be written in a single write cycle.
Current Address Read	: Read the current address (Internal Address Counter).
Random Address Read	: Read the addressed byte.
Sequential Read	: Read up to 256 bytes in a single cycle.

/// SR 10 BP / PR 09 BP PIN OUT



Pin HE 10 n°	SR 10 BP	PR 09 BP *
● 1	Key switch (switch)	A1
● 2	Key switch (switch)	A2
● 3	A1	A0
● 4	A2	VSS
● 5	A0	Key switch (potential) *
● 6	VSS	NC
● 7	VDD	VDD
● 8	SDA	SDA
● 9	Mode	Mode *
● 10	SCL	SCL

* NB – If the MK40C key is used with the PR 09 BP connector, this will automatically bring pins 5 and 9 to the same potential.

SEFEA reserves the right to modify the technical characteristics of the equipment described in this documentation at any time

E.mail : info@sefea.com – Web site : www.sefea.com

A company of the SWATCH GROUP of Switzerland

15, rue de Valeury. 74106 ANNEMASSE - FRANCE Tel : ++ 33 450 87 61 61 - Fax : ++ 33 450 87 61 60